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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/611,868	07/03/2003	Kazuhiro Nishikawa	2003_0915A	4173
513	7590 01/26/2005		EXAM	INER
WENDEROTH, LIND & PONACK, L.L.P.			ARBES, CARL J	
2033 K STRE	ET N. W.			
SUITE 800			ART UNIT	PAPER NUMBER
WASHINGTON, DC 20006-1021			3729	

DATE MAILED: 01/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	A III A A					
	Application No.	Applicant(s)				
	10/611,868	NISHIKAWA ET AL.				
Office Action Summary	Examiner	Art Unit				
	C. J. Arbes	3729				
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address				
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, and If NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by standard patent term adjustment. See 37 CFR 1.704(b).	ON. R 1.136(a). In no event, however, may a n. a reply within the statutory minimum of thir eriod will apply and will expire SIX (6) MON tatute, cause the application to become A	reply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 0	<u>3 July 200</u> 3.					
,	This action is non-final.					
3) Since this application is in condition for allo	, _					
Disposition of Claims						
4) Claim(s) 1-17 is/are pending in the applicated 4a) Of the above claim(s) is/are with 5) Claim(s) 1-15 is/are allowed. 6) Claim(s) 16 and 17 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction are	drawn from consideration.					
Application Papers	•					
9) The specification is objected to by the Exam 10) The drawing(s) filed on is/are: a) Applicant may not request that any objection to Replacement drawing sheet(s) including the co. 11) The oath or declaration is objected to by the	accepted or b) objected to the drawing(s) be held in abeya rrection is required if the drawing	nce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the papplication from the International Bu * See the attached detailed Office action for a	nents have been received. nents have been received in A priority documents have beer reau (PCT Rule 17.2(a)).	Application No received in this National Stage				
Attachment(s)	_					
1) Notice of References Cited (PTO-892)	•	Summary (PTO-413) s)/Mail Date	•			
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SE Paper No(s)/Mail Date 	,	nformal Patent Application (PTO-152)	•••			

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The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim16 and 17 are 35 U.S.C. 103(a) as being unpatentable over Pelligrino (Pat No 4,606,787).

Pelligrino teaches a method for making high density multilayered printed circuit boards wherein conductive circuit patterns are embedded and are integral with insulating material, such that the surface of the circuit pattern is exposed along the surface of the substrate and lays flush there. At least two (2) boards are stacked. The assembly is heat-pressed together to form a homogeneous block of insulator material and having conductive circuit patterns embedded therein. Figure 14 depicts a lead hole or interconnect which can be provided for the mutilayered circuit board. By established legal precedent, it is unnecessary for prior art to show any steps which are performed for a proper rejection of a product-by-process claim and therefore if Pelligrino does not anticipate these claims the evidence in this prior art surely makes these product claims obvious.

Claims 1-15 are held to be allowable.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to C. J. Arbes whose telephone number is 571-272-4563. The examiner can normally be reached on M. T, R and F from 8 to 6. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, P. Vo,

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can be reached. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

C. J. Arbes
Primary Examiner
Art Unit 3729